

Study the Impact of Thickness on Electrical Properties of Spin Coated CuO Thin Films

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Abstract

Copper oxide (CuO) thin films are gaining significant attention in various technological applications due to their distinctive electrical properties. As a p-type semiconductor with a narrow bandgap, CuO is particularly valuable in the development of electronic devices such as transistors, diodes, and sensors. In this study, we investigate the influence of film thickness on the electrical properties of spin-coated CuO thin films. Thin films were prepared using a spin coating technique on glass substrates, with thicknesses ranging from 53 nm to 64 nm. The thickness of films was measured by low cost weight difference method. The electrical properties such as resistivity, activation energy and TCR were evaluated using a half bridge technique. Obtained results reveal a systematic decrease in resistivity with increasing film thickness. This study underscores the vital role of film thickness in modulating the electrical behavior of CuO thin films, highlighting their potential for applications in electronic devices and sensors.

Keywords: Copper oxide, thin films, glass substrates, thicknesses, weight difference method.

1. Introduction:

Metal oxide semiconductors (MOS) are pivotal in modern electronics, owing to their unique electrical properties, chemical stability, and wide-ranging bandgaps [1]. These materials, which include oxides such as titanium dioxide (TiO₂), zinc oxide (ZnO), tin oxide (SnO₂), and copper oxide (CuO), are integral to the development of various electronic devices. Unlike traditional semiconductors like silicon, metal oxides offer greater flexibility in tuning their electrical properties through doping, defect engineering, and nanostructuring [2, 3]. This versatility allows for the fabrication of devices that operate under diverse conditions, including high temperatures and harsh environments. The ability to modify their electrical, optical, and chemical properties makes metal oxide semiconductors indispensable in the advancement of semiconductor technology [4]. One of the most significant applications of metal oxide semiconductors is in sensing technologies. Metal oxide semiconductors are particularly well-suited for gas sensors due to their high sensitivity to various gases, such as carbon monoxide, nitrogen dioxide, and volatile organic compounds. The electrical conductivity of these materials changes in response to gas adsorption on their surface, enabling the detection of low gas concentrations [4, 5]. For instance, zinc oxide (ZnO) is widely used in gas sensors for detecting toxic gases, while tin oxide (SnO₂) and CuO both are common in commercial gas sensors for detecting pollutants. Additionally, metal oxide semiconductors are employed in humidity sensors, where the change in electrical resistance with varying moisture levels enables precise environmental monitoring. Metal oxide semiconductors also play a critical role in the field of photovoltaics and energy harvesting. Titanium dioxide (TiO₂) and CuO are a prominent materials used in dye-sensitized solar cells (DSSCs), where it serves as the photoanode. The wide bandgap and excellent electron mobility of CuO facilitate efficient light absorption and electron transport, enhancing the overall efficiency of the solar cell. Moreover, metal oxide semiconductors are explored in perovskite solar cells as electron transport layers, contributing to the high power conversion efficiencies of these next-generation photovoltaic devices. Beyond solar energy, metal oxides like zinc oxide are also employed in piezoelectric nanogenerators, where they convert mechanical energy into electrical energy, showcasing their potential in energy harvesting applications [5-7]. Transparent electronics is another

emerging field where metal oxide semiconductors have made significant contributions. Materials like indium tin oxide (ITO) and CuO are transparent conductors, crucial for fabricating devices like transparent displays, touch screens, and transparent transistors [8, 9]. The combination of optical transparency and electrical conductivity in these metal oxides allows for the development of invisible electronic circuits that integrated into windows, flexible displays, and wearable devices [8, 10]. The potential for high mobility, environmental stability, and the ability to be processed at low temperatures make metal oxide semiconductors ideal for next-generation transparent electronics [11, 12].

CuO nanoparticles exhibit unique properties that make them highly valuable in various scientific and industrial applications. These nanoparticles are characterized by their p-type semiconducting behavior, high thermal and electrical conductivity, and significant catalytic activity [13, 14]. With a narrow bandgap of approximately 1.2–1.9 eV, CuO nanoparticles are highly effective in absorbing visible light, making them suitable for photocatalytic and photovoltaic applications [13, 14]. The synthesis of CuO nanoparticles could be achieved through various methods, including sol-gel, hydrothermal, co-precipitation, and chemical vapor deposition techniques. Each method allows control over the particle size, shape, and surface area, which are crucial factors in determining their performance in different applications [14, 15]. CuO nanoparticles are extensively used in gas sensors due to their high sensitivity to gases like hydrogen and carbon monoxide. They also play a crucial role in catalysis, particularly in environmental remediation processes such as the degradation of organic pollutants [16-18]. Additionally, CuO nanoparticles are explored in biomedical applications, including antimicrobial agents and drug delivery systems, due to their bioactivity and ability to interact with biological molecules. The versatility and wide-ranging applicability of CuO nanoparticles continue to drive research and innovation in various fields [18, 19].

The spin coating technique is a widely used method for the preparation of thin films, offering a simple, cost-effective, and highly controllable process for fabricating uniform layers on various substrates. In this technique, a solution containing the desired material is deposited onto the center of a substrate, which is then rapidly rotated at high speeds [20, 21]. The centrifugal force generated by the spinning motion spreads the solution evenly across the surface, while excess liquid is expelled, resulting in the formation of a thin film. The final thickness of the film is primarily determined by the spin speed, spin time, and the viscosity of the solution. Spin coating is particularly advantageous for producing thin films with well-defined thicknesses and smooth surfaces, making it ideal for applications in electronics, optics, and nanotechnology. It is commonly used to fabricate thin films of semiconductors, dielectrics, and organic materials for devices such as transistors, solar cells, and sensors [21, 22]. The versatility of spin coating allows for the integration of various materials into complex multilayer structures, enhancing the performance and functionality of thin-film devices [23].

The major aim of the current research work is to prepare CuO thin films at different RPM using spin coating technique and study the impact of thickness on electrical properties of prepared CuO thin films.

2. Experimental work

2.1 Synthesis and preparation of CuO thin films

All AR grade chemicals were used without further purification for synthesis and preparation of CuO thin films. $\text{CuCl}_2 \cdot 2\text{H}_2\text{O}$ was used as source of CuO. The $\text{CuCl}_2 \cdot 2\text{H}_2\text{O}$ (0.1 M) was dissolved in 100 ml in cleaned Beaker. 1 ml of glacial acetic acid is added to above aqueous solution of $\text{CuCl}_2 \cdot 2\text{H}_2\text{O}$. Prepared solutions were mix through the constant stirring using magnetic stirrer at 60 °C 8 M. After that, 0.1 M NaOH solution was added to above heated solution till pH reaches to ~ 10.30. The colour of solution turned from blue to black instantly and then large amount of black precipitate gel was formed [24, 25]. The obtained gel was used to prepare thin films of CuO at different RPM (revolutions per minute). After synthesizing the gel containing CuO precursors via the sol-gel method, the obtained gel was utilized to prepare thin films of CuO using the spin coating technique. The gel was first diluted to achieve the desired viscosity, ensuring smooth application onto the glass substrates. This solution was then carefully deposited onto the surface of cleaned glass substrates. To explore the effect of rotational speed on the film properties, the substrates were spun at various rotation speeds (1000, 2000, 3000 and 4000 RPM) during the spin coating process. Because in spin coating, RPM is a key parameter that directly influences the thickness of the thin film being applied. Selected different RPMs were employed to control the thickness of the resulting CuO thin films. The coated films were

subsequently dried under IR lamp for 30-40 minutes and then annealed at range of 400 °C temperature using muffle furnace then used further characterizations [25, 26].

2.2 Electrical characterizations of CuO films by half-bridge technique

The electrical characterizations of prepared CuO thin films were investigated by using the half-bridge technique [27]. The schematic of half-bridge technique and circuit diagram is revealed in Fig. 1.

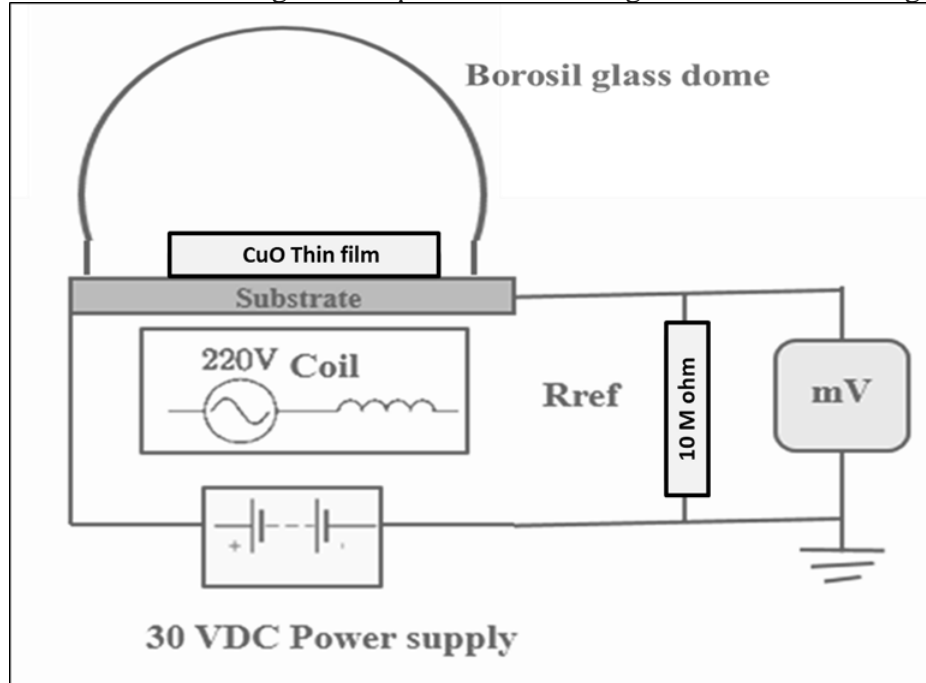


Figure 1: Schematic of half-bridge technique

Result and Discussion

The prepared CuO thin film samples at 1000, 2000, 3000 and 4000 RPM are labelled as Cu1, Cu2, Cu3 and Cu4 in this section. The thickness of the CuO thin films was estimated using the weight difference technique (Eq. 1), a straightforward and reliable method commonly employed in thin film studies. In this approach, the substrate is weighed before and after the deposition of the film. The difference in mass is attributed to the mass of the deposited film. To calculate the thickness, the mass of the film is divided by the area of the substrate and the density of the material [27, 28]. The estimated thickness of CuO films is shown in Fig. 1.

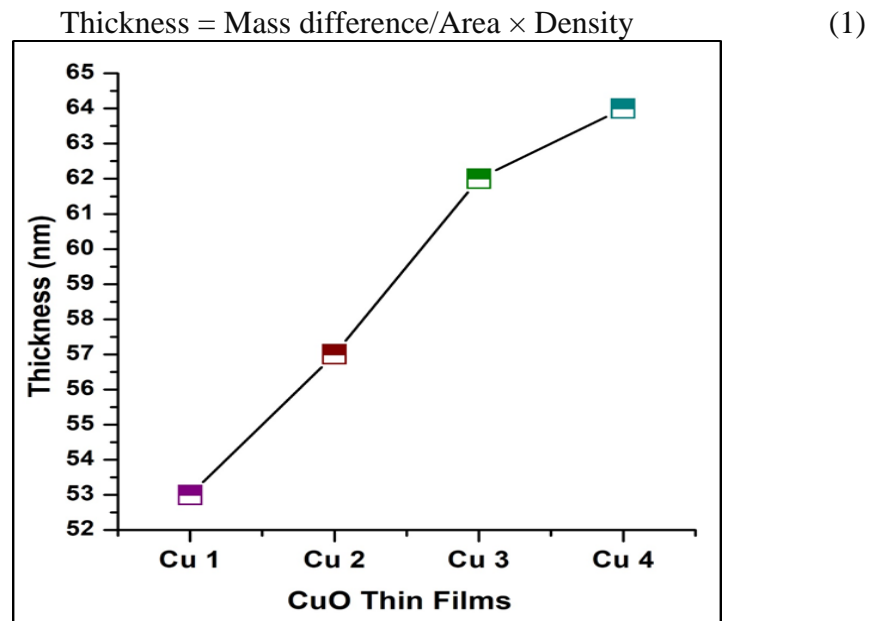


Figure 2: Thickness of CuO thin films

From Fig. 2, it is found that as the RPM increases the thickness of films is increases. It could be because of a solution with higher viscosity lead to a thicker film. A more viscous solution has a higher resistance to flow and spreads less easily during spinning, resulting in a thicker deposit on the substrate [24, 25]. Higher RPMs generally result in thinner films because the increased centrifugal force spreads the coating solution more thinly across the substrate.

The resistance versus temperature plot is shown in Fig. 3. The resistance of film is calculated by using Eq. 2.

$$R_{\text{sample}} = R_{\text{ref}} \left[\frac{(V_{\text{supply}})}{(V_{\text{ref}})} - 1 \right] \tag{2}$$

Where, R_{sample} – film resistance, V_{supply} – Applied voltage, and V_{ref} – Voltage across external resistor R_L . Fig. 3 shows that the resistance of the film found to be decreases as surrounding temperature raised. This nature of films suggesting the semiconductor behaviour of films. As the temperature increases, the thermal energy provided to the charge carriers (holes in the case of p-type CuO) increases their mobility. Higher mobility allows charge carriers to move more freely through the material, thereby reducing the film’s electrical resistance. As temperature rises, thermal activation promote more charge carriers into the conduction band. This increase in the number of charge carriers enhances the material's conductivity, resulting in lower resistance [27, 29].

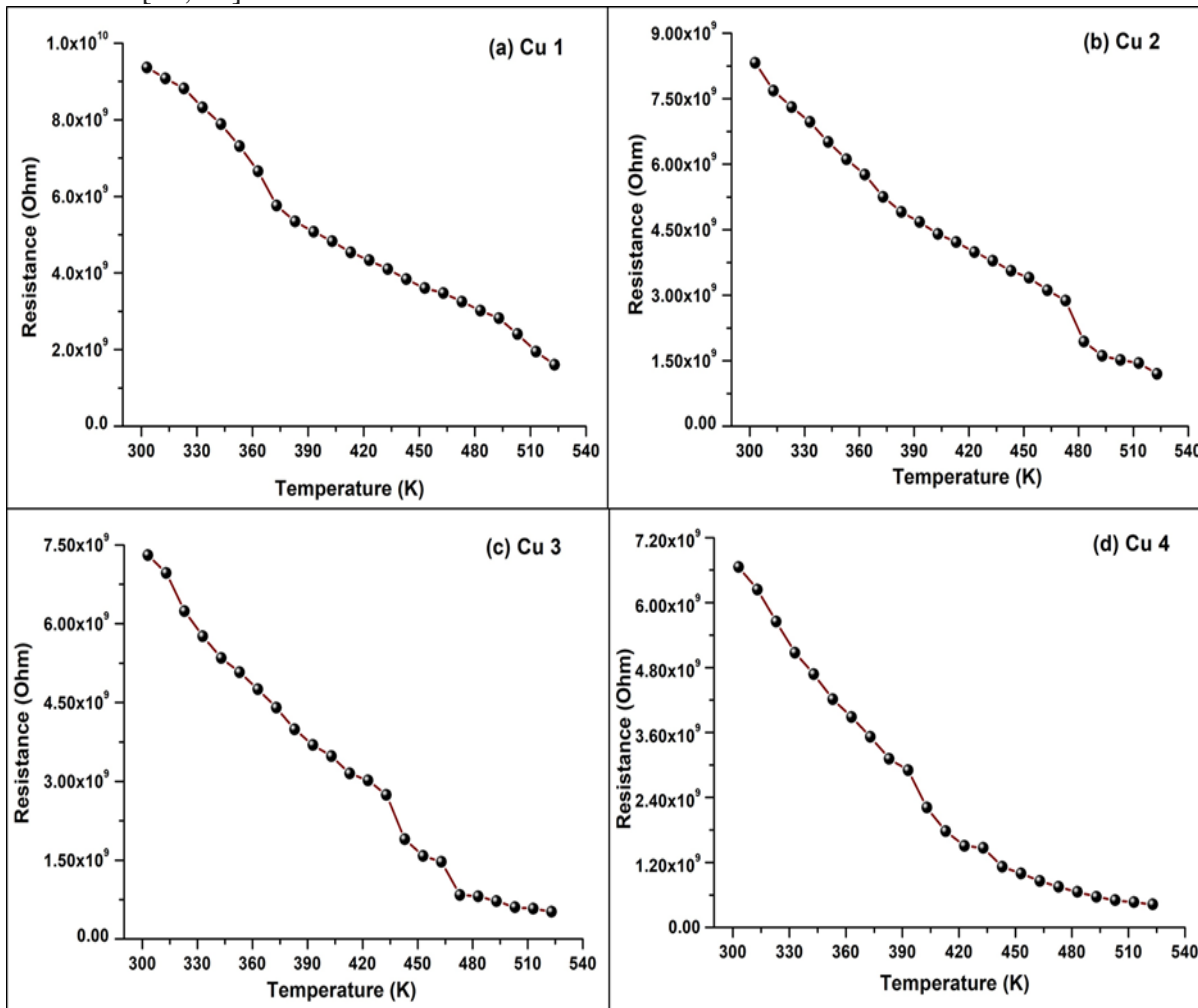


Figure 3: Resistance versus temperature plot of CuO thin films

Equation 3 was used to determine resistivity of prepared films.

$$\rho = \left(\frac{R \times b \times t}{l} \right) \Omega - m \tag{3}$$

Where, ρ = Resistivity of prepared film, R = resistance at normal temperature, b = breadth of film, t = thickness of the film, L = length of the film.

Fig. 4 illustrate the resistivity versus thickness (CuO thin films sample) plot. The resistivity of spin coated CuO thin films is increased as thickness increased. It may be an increased probability of charge carrier recombination, especially if the film is not well-optimized. Recombination of charge carriers reduces the number of free carriers available for conduction, leading to higher resistivity [29, 30].

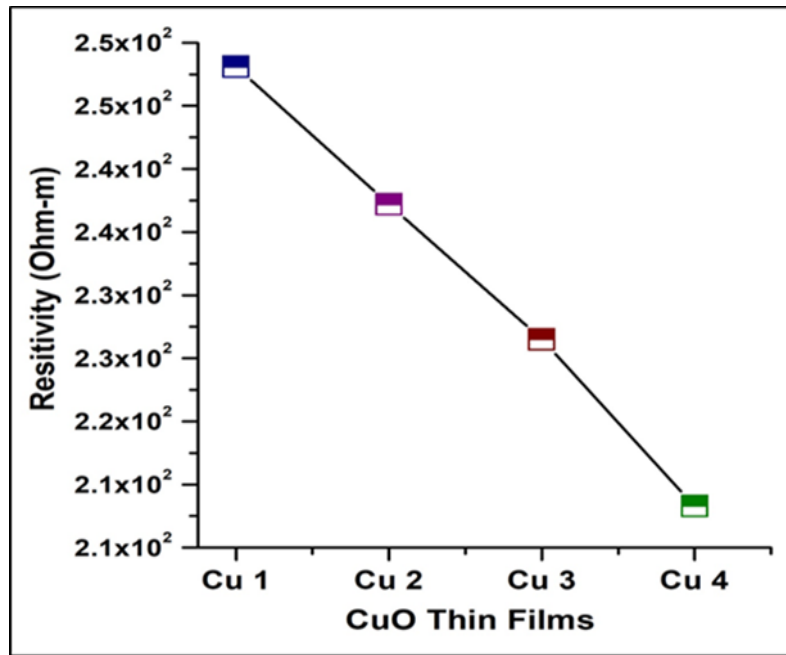


Figure 4: Resistivity versus CuO thin film

Also the distance that charge carriers must travel through the material is longer. As they traverse this increased path length, they are more likely to encounter scattering centers and other obstacles that contribute to increased resistivity. The TCR of films calculated by Eq. 4.

$$TCR = \frac{1}{R_o} \left(\frac{\Delta R}{\Delta T} \right) / ^\circ C \tag{4}$$

Where, ΔR = change in resistance between temperature T_1 and T_2 , ΔT = temperature difference between T_1 and T_2 and R_o = room temperature resistance of the film.

The activation energy of spin-coated CuO thin films is a key parameter that describes how the film’s resistivity changes with temperature [27]. It provides valuable information about the charge transport mechanisms and is crucial for the practical application and optimization of CuO thin films in electronic and optoelectronic devices. Eq. 5 is used to calculate activation energy of prepared CuO films.

$$\Delta E = Ae^{-Ea/kBT} \text{ eV} \tag{5}$$

Where, ΔE = Activation energy, T = Temperature in Kelvin and A = Arrhenius prefactor.

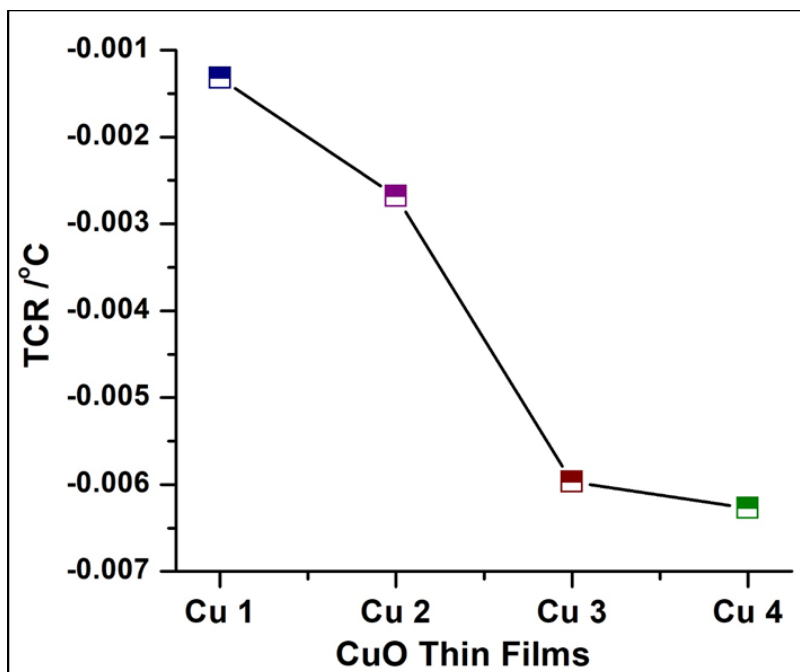


Figure 5: TCR versus CuO thin film

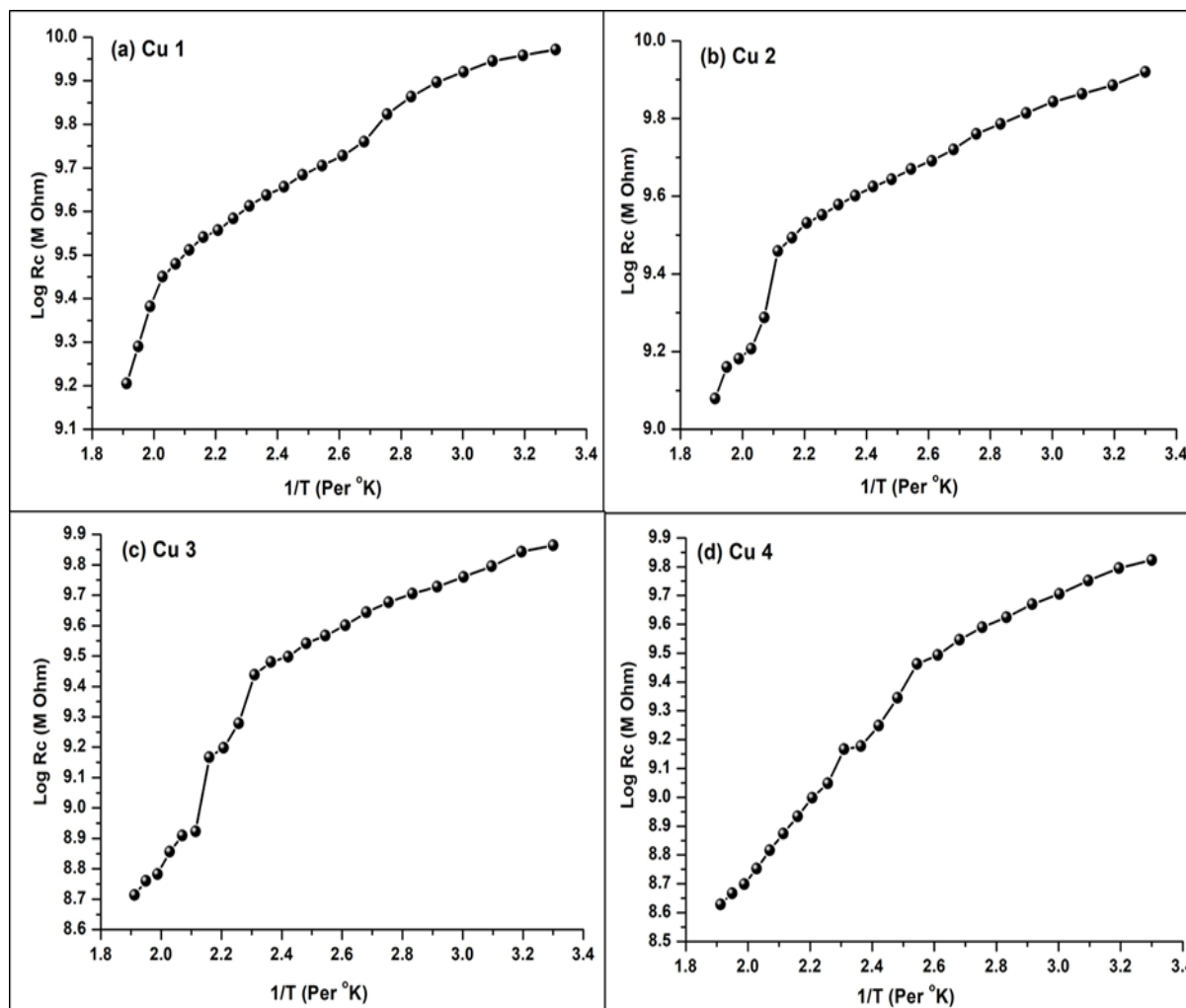


Figure 6: Log Rc versus 1/T plot of CuO thin films

It is observed that, as the thickness of the CuO thin films increases, the defect density often rises, including grain boundaries, dislocations, and voids. These defects impact the charge carrier transport mechanisms and, consequently, the activation energy. Higher defect densities decrease the activation energy depending on whether they act as traps or scattering centers. The activation energy of spin-coated CuO thin films is influenced by thickness through changes in defect density, microstructure, carrier transport mechanisms, and film quality [27-29].

The estimated electrical parameters of CuO thin films are tabulated in Table 1.

Table 1: Electrical outcomes of CuO thin films

Sample	Thickness (nm)	Resistivity ($\Omega.m$)	TCR ($/^{\circ}C$)	Activation Energy (eV)	
				HTR	LTR
Cu 1	53	248.1	-0.00132	0.3116	0.0532
Cu 2	57	237.2	-0.00268	0.2086	0.0551
Cu 3	62	226.5	-0.00597	0.2447	0.0738
Cu 4	64	213.3	-0.00627	0.2449	0.0903

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Conflicts of Interest:

The author declare no conflict of interest.

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