DC-DC BOOST CONVERTERS - A LITERATURE REVIEW

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Abstract: In recent years, as a consequence of fast paced populace and industrialization, we can see a steady increase in the energy performance in all the fields. But the warehouse of energy resources such as fossil fuels etc are getting lesser and lesser. These energy resources are safer to use and are non-renewable. The consumption of fossil fuel has certain disadvantage that by its dissipation greenhouse wastes are collected in the atmosphere which is the major cause of global warming and climatic alterations. In this scenario renewable resources like solar cells, fuel cells, wave energy and tidal energy are becoming more acceptable alternate energy sources in distribution system. The hazard of collapse of fossil fuels can be conquered to a great extent as the renewable energy source can be taken as an alternate because it is available in excessive in nature. But the voltage delivered by such renewable energy sources are unstable and very low to be utilised for commercial purposes. So they are combined with high gain dc-dc converters to get a high output voltage. The high output voltage obtained from the dc-dc converter is then assembled to the inverter for high ac power production.

Keywords: Voltage Multiplier Cell, Interleaved Boost Converter, coupled inductor, Switched capacitor.

1. INTRODUCTION

In Commercial and Industrial applications, it is required to convert a constant dc voltage into variable voltage dc. A dc-dc converter can be considered as the dc equivalent of an AC transformer with varying turns ratio. The basics of boost converters are given in [2] to understand the basic working principle which deals with time domain studies, filter design, duty cycle derivation, signal stability analysis etc

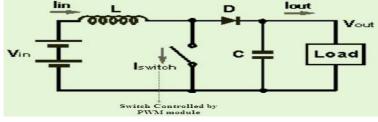


Figure1: Basic Boost converter circuit [23]

The basic boost converter circuit consist of inductor, MOSFET as a power switch, diode, capacitor and a resistive load. Square wave is given as input to the MOSFET gate. MOSFET acts as a switch. It conducts and the inductor stores energy in the magnetic field. During the falling edge of the square wave, MOSFET gets turned off. The sudden current fall causes the production of back emf. Here the input voltage and the back emf comes in series with each other and get added. The boost converters require large duty cycles to produce high voltages which in turn causes high conduction loss in MOSFET and the reduction in converter efficiency.

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2. VOLTAGE BOOSTING METHODS

Literature survey reveals that there are so many methods to boost voltage level with high efficiency. The methods include switched capacitor, Voltage multiplier cells, switched inductor for voltage lift, magnetic coupling and multistage level. This paper makes a review of different topologies making use of these various methods.

Recently many applications are concentrated on battery power and low voltage storage elements. Hence the demand for efficient, high gain dc-dc boost converters are increasing day by day. Typical applications are photovoltaic array, fuel cell stack, renewable energy systems, Electric vehicles, home dc appliances etc., uninterrupted power supply [3]-[5]. Converters with high static gain, high efficiency, less weight, volume and cost is required for these applications. In order to get high efficiency, the crucial stage for these converters are the voltage boosting stage due to the operation with high input current and high output voltage.

2.1 Switched Capacitor technique

A switched capacitor is an electronic circuit which utilises the movement of charges into and out of capacitors when switches are opened and closed. It is an effective method for obtaining high gain [6]. The main advantage is that the energy stored in the magnetic elements is very less by which weight, size and cost for the inductors are substantially reduced, less conduction loss for the power supply. and improved efficiency. The main drawback of the switched capacitor technique is the pulsating input current. Many other converters based on switched capacitor technique is discussed [6]-[8]. Many high voltage gain converter topologies have been developed which utilises the combination of conventional boost converters with switched capacitor-inductor technique.[4][5][9]. [24] suggests a system which can be used in distribution system which facilitates the storage of high voltage in grid system. Here the switched inductor in the second stage provides high gain.

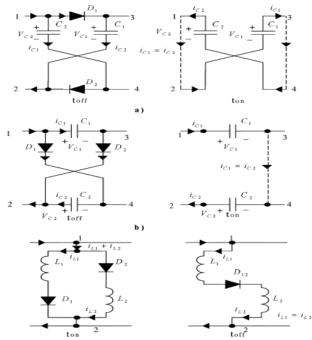


Figure 1. Switching topologies of the step-up structures [6]

2.2 Voltage Lift Converters

It is the most effective voltage boosting technique which uses the performance of energy storing elements such as capacitors and inductors. Capacitor is charged and Step by step increase of the input voltage charges

the capacitor more and more and deliver high voltage to load. Figure 2 shows the basic circuit of voltage lifting method.

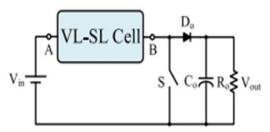


Figure 2: Voltage lifting basic circuit [12]

As the number of capacitors is increased, the voltage can be re-lifted, triple-lifted and quadruple lifted. A boost converter is proposed using this technique [10]. The main advantages are reduced output voltage, reduced complexity due to the lack of additional switches, high efficiency, low cost. [11] uses a combination of coupled inductors and voltage lift cell. The main advantages are high voltage conversion ratio is obtained through inversely coupled inductors and voltage lift cell.

2.3 Voltage multiplier technique

A voltage multiplier cell (VMC) includes a number of of capacitors and diodes for attaining high voltage gain. It can be placed in the converter in two ways. 1. Place it after the switch by which the voltage stress across the switch is highly reduced. The second method is to place the VMC in the output stage of the transformer. Figure.2 shows the placement of VMC in step up converter.

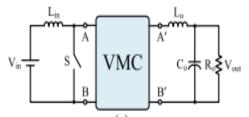


Figure 2: Placement of VMC in step up converter [12]

A conventional boost converter coupled with a voltage multiplier cell (VMC) is suggested. [4][13] proposes a boost converter having a power switch, a three -winding inductor, and a voltage multiplier unit. A high voltage gain can be achieved by avoiding the need of high duty cycle. A step- up DC-DC converter providing high gain which uses three inductors which are coupled to each other and a voltage multiplier system are proposed to obtain high output voltage [15]. Here the number of active switches required is only one which avoids the need of high duty cycle. To reduce the voltage stress on the power switch and diodes, the secondary windings turns ratio is increased. Hence low rate switches can be selected. A network which cancels out the ripples is proposed [15]. The proposed network includes capacitors and coupled inductors and it achieved both the merits of the interleaved boost converter and input current ripple cancellation. A standard boost converter using voltage multiplier cell is introduced in [15][16]. A high voltage gain is attained by this topology.

2.4 Magnetic Coupling Technique

Since the foremost feature of the renewable energy sources are their low output voltage, they are combined with high gain dc-dc converters to get a high output voltage [1]. A non-isolated step up dc to dc converter is introduced. A regenerative snubber circuit recycles the leakage current to the output side. Soft switching conditions are used here. The high output voltage thus obtained is then assembled to the inverter for ac power

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production. In a standard boost converter as the voltage conversion ratio increases, the duty ratio also increases. So in an attempt to meet the high voltage requirement, high duty ratio is required by the conventional boost converter. In such a circumstance it is very difficult to get high efficiency. The ordinary boost converters face with certain disadvantages like the high conduction losses, diode reverse recovery problem which in sequence decreases the efficiency and low power level. Boost converters combined with coupled inductors are a good remedy for the above mentioned problems. Wisely selecting the turns ratio of the coupled inductors, the duty ratio can be minimised and hence the voltage stress on the switch. A few papers in the literature concerns with the boost converters with coupled inductors combined with transformer which has turns ratio of greater than one. High gain is obtained with these converters without using high duty ratio and more components [4][5][9]. Practical coupled inductor acquires huge measure of leakage inductance because there exists a non-ideal coupling between primary and secondary. The power switches are affected by the voltage stress which is a result of the leakage inductance. The paper [16] has been proposed as a solution. But the active clamp topologies were complex. For applications utilizing high input current, an interleaved boost converter comprising of coupled inductors is proposed [17] in the literature.

2.5 Interleaved technique

In interleaving method, the frequency of a phase is lower than the effective switching frequency. The switching losses are substantially reduced by the interleaving method as the input current is divided into various phases. An interleaved converter with voltage multiplier cell is introduced [18]. The voltage gain range can be increased by adjusting the turns ratio of the coupled inductor. The main limitations of this converter is hard switching operation of the MOSFET switch and large current stress through the clamp capacitors. An interleaved converter [19] is recommended in the literature. The energy stored in the inductance is gathered together in a clamp capacitor. The clamp diode is acitng as the discharging path for the leakage current. Thus the clamp operation is done efficiently. A high gain interleaved dc-dc converter having two multiplier cell is introduced in the literature [20]. But the main drawback found is that the system is so bulky as it consists of so many power capacitors and power diodes. Non-isolated converters using voltage multiplier cells and switched capacitor method have been suggested [25] which can provide high voltage gain having less turns ratio at very low duty cycle. In this topology the size of component can be effectively reduced. In dc- dc converters the power switches are affected by voltage spikes due to the presence of leakage energy. As a solution to this problem snubber circuit utilizing active clamp network is brought forward [22].

3 COMPARISON OF DIFFERENT VOLTAGE BOOSTING METHODS

Voltage boosting	Advantage	Disadvantage	Application Field
Method			
	High gain, fast	Pulsating input	Energy
Switched Capacitor	response, high	current	harvesting,power
	conversion		electronic applications
	ratio		
	Low voltage		
	stress on		
	semiconductor	Complex	
	devices, high	circuit,	Renewable energy
Voltage Lift	voltage	reduced	applications
	conversion	efficiency,	

Table 1: Comparison of different voltage boosting methods.

	ratio, high		
	-		
	power density.		
	Less ripples in		
	output voltage		Γ
	High voltage	Need multiple	
	gain, Reduces	sections for	
	the stress on	high voltage	
	main	applications,	
	switch.Compact	high voltage	X-Ray, Laser, Plasma
Voltage Multiplier	circuit, can be	stress on	research
	integrated with	components	
	interleaved	1	
	converters,		
	modular		
	structure		
	High voltage	Unwanted	Renewable energy
	gain,applicable	voltage spikes	generators,grid
	to isolated and	across the	applications
Magnetic Coupling	non-isolated	switches.	apprications
	converters.	leakage	
	conventers.	inductance.	
	TT' 1 '		
	High gain,	Complex	
	reduction of	Structure, High	
Multistage	switching loss,	stress on	Renewable energy
(Interleaved)	increased power	semiconductor	systems, high power
	density, reduced	devices,	applications
	input and output	additional	
	ripples.	current loops	
		required.	

Conclusion

This paper makes a literature review of different dc-dc boost converter topologies presented in many international journals, IEEE transactions and some conference papers. Different topologies were studied for understanding the operations, converter techniques, advantages and disadvantages. The study makes a reveal of the best topologies which gives high gain, high efficiency and less ripples which are considered as the main factors in many applications like photovoltaic cells, Hybrid electric vehicles, Renewable energy systems etc.

REFERENCE

[1] M.Forouzesh, Y. Shen.K. Yari.Y.P. Siwakoti and F. Blaabjerg. 'High Efficiency High Step-Up DC-DC Converter with Dual Coupled Inductors for grid connected photovoltaic system' IEEE Transactions On Power Electronics, 2017.

[2] The text book 'Pulse width modulated DC-DC Converters' by Keng Chih Wu.Staff Engineer, Power Electronics, Lockheed Martin Missile and Space.

[3] Q. Zhao and F. C. Lee, "High-efficiency, high step-up dc-dc converters," IEEE Trans. Power Electron., vol. 18, no. 1, pp. 65–73, Jan. 2003.

[4] Y. Ye, K. Cheng, and S. Chen, "A High Step-up PWM DC-DC Converter With Coupled-Inductor and Resonant Switched-Capacitor," IEEE Transactions on Power Electronics, vol. 32, pp. 7739-7749, 2017

[5] X. Zhu, B. Zhang, Z. Li, H. Li, and L. Ran, "Extended Switched-Boost DC-DC Converters Adopting Switched-Capacitor/Switched-Inductor Cells for High Step-up Conversion," IEEE Journal of Emerging and Selected Topics in Power Electronics, vol. 5, pp. 1020-1030, 2017.

[6] B. Axelrod, Y. Berkovich, and A. Ioinovici, "Switched capacitor/switched-inductor structures for getting transformerless hybrid DC–DC PWM converters," IEEE Transactions on Circuits and Systems I: Regular Papers, vol. 55, pp. 687-696, 2008.

[7]Tse, C.K.; Wong, S.C.; Chow, M.H.L., "On lossless switched-capacitor power converters," in Power Electronics, IEEE Transactions on , vol.10, no.3, pp.286-291, May 1995

[8] Makowski, M.S.; Maksimovic, D., "Performance limits of switched-capacitor DC-DC converters," in Power Electronics Specialists Conference, 1995. PESC '95 Record., 26th Annual IEEE Vol.2, no., pp.1215-1221 vol.2, 18-22 Jun 1995

[9] G. Wu, X. Ruan, and Z. Ye, "Non-Isolated High Step-Up DC-DC Converters Adopting Switched-Capacitor Cell," 2014.

[10] Nihala K Shereef, Neena Mani, Neetha John "A Novel DC-DC Boost Converter Based on Voltag-Lift Technique" Volume 11 Issue 3 November 2018

[11] D. R. Nayanasiri, Member IEEE and Thilina. S. Ambagahawaththa "Boost Converter Based on Coupled Inductor and Voltage Lift Cell" Proc. of the 2017 IEEE Region 10 Conference (TENCON), Malaysia, November 5-8, 2017

[12] Mojtaba Forouzesh, IEEE, Yam P. Siwakoti "Step-Up DC–DC Converters: A Comprehensive Review of Voltage-Boosting Techniques, Topologies, and Applications" IEEE Transactions on Power Electronics, VOL.
32, NO. 12, December 2017

[13] T. Nouri, S. H. Hosseini, E. Babaei, and J. Ebrahimi, "Interleaved high step-up dc–dc converter based on three-winding high-frequency coupled inductor and voltage multiplier cell," IET Power Electronics, vol. 8, pp. 175-189, 2014

[14] S. Salehi, S. Dehghan, and S. Hasanzadeh "Ultra step-up DC-DC converter based on three windings coupled inductor," in Power Electronics and Drive Systems Technologies Conference (PEDSTC), 2016 7th, 2016, pp. 171-176.

[15] Y. Gu and D. Zhang, "Interleaved boost converter with ripple cancellation network," IEEE transactions on power electronics, vol. 28, pp. 3860-3869, 2013

[16] G. Spiazzi, P. Mattavelli, and A. Costabeber. 'High Step-Up ratio flyback converter with active clamp and voltage multiplier:, IEEE Transactions on Power Electronics, Volume .26 ,Issue 11 , Nov.2011

[17] X. Hu and C. Gong, "A high gain input-parallel output-series DC/DC converter with dual coupled inductors," IEEE transactions on Power Electronics, vol. 30, pp. 1306-1317, 2015.

[18] Wuhua Li, Member, IEEE, Yi Zhao, Yan Deng, and Xiangning He, Fellow, IEEE "Interleaved Converter With Voltage Multiplier Cell for High Step-Up and High-Efficiency Conversion" IEEE Transactions on Power Electronics, VOL. 25, NO. 9, SEPTEMBER 2010

[19] Suman Dwari, Student Member, IEEE, and Leila Parsa, Member, IEEE 'An Efficient High-Step-Up Interleaved DC–DC Converter With a Common Active Clamp' IEEE Transactions on Power Electronics, VOL. 26, NO. 1, JANUARY 2011

[20] Marcos Prudente, Luciano L. Pfitscher, Gustavo Emmendoerfer, Eduardo F. Romaneli, and Roger Gules 'Voltage Multiplier Cells Applied to Non-Isolated DC–DC Converters'IEEE Transactions on Power Electronics, VOL. 23, NO. 2, MARCH 2008.

[21] Seyed Mohsen Salehi, Seyed Mohammad Dehghan, Saeed Hasanzadeh 'Interleaved-Input Series-Output Ultra High Voltage Gain DC-DC Converter' IEEE Transactions on power electronics, DOI: 10.1109/TPEL.2018.2853577

[22] W. Li, L. Fan, Y. Zhao, X. He, D. Xu, and B. Wu, "High-step-up and high-efficiency fuel-cell powergeneration system with active-clamp flyback-forward converter," IEEE Transactions on Industrial Electronics, vol. 59, pp. 599-610, 2012

[23] www.elprocus.com/12V to 24 V-dc-converter-using-lm324

[24] Cheng-I Chen and Zhen-Ting Shao, Yeong-Chin Chen 'A DC-DC Boost Converter with High Voltage Gain for Distributed Generation" 2016 IEEE 5th Global Conference on Consumer Electronics